Amendments to the Specification:

Please replace paragraph [0029] with the following amended paragraph:

[0029] In addition to storing video data in the memory unit 231, each of the image converter blocks has the ability to read video data from the memory unit 231 and processes the data accordingly. In either case, each of the image converter blocks can then be used to pass the video data read from the memory unit 231 to an image compositor unit as needed. If the frame rates are different, then frame rate conversion is performed by writing video data into the memory at a first frame rate and read out at a display rate. By providing memory resources to each of the image converter units, the output frame rates have the same clock which could be locked to any of the input video data streams (i.e., Φ_{vid}, Φ_{ui}, or Φ_{prog}) or could be free running in that each data stream comes in at its own rate but is locked to a display rate or ~~ration~~ <u>ratio</u> of rates that may be than any of the incoming rates.